

Fig. 1 Prior art

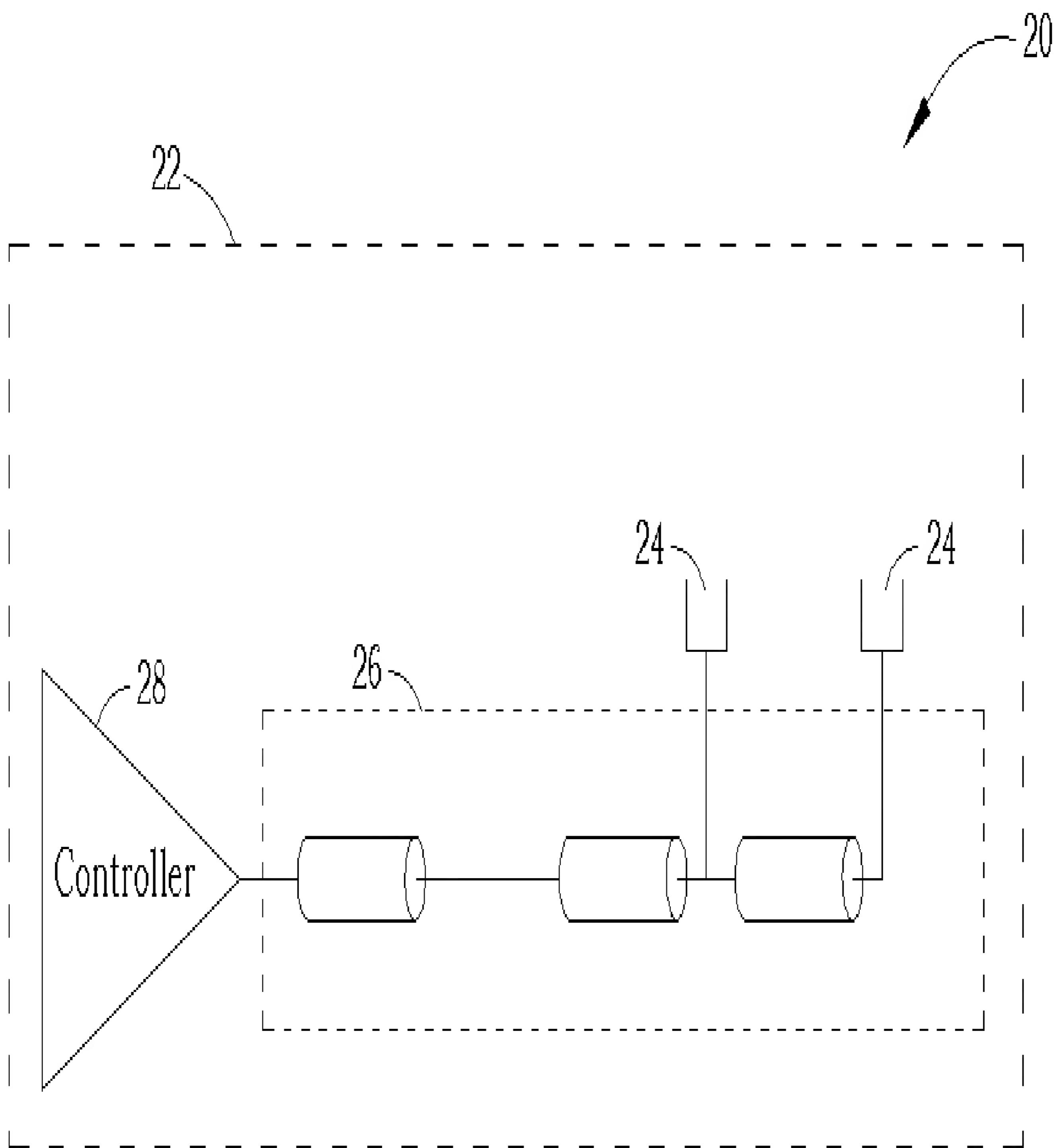
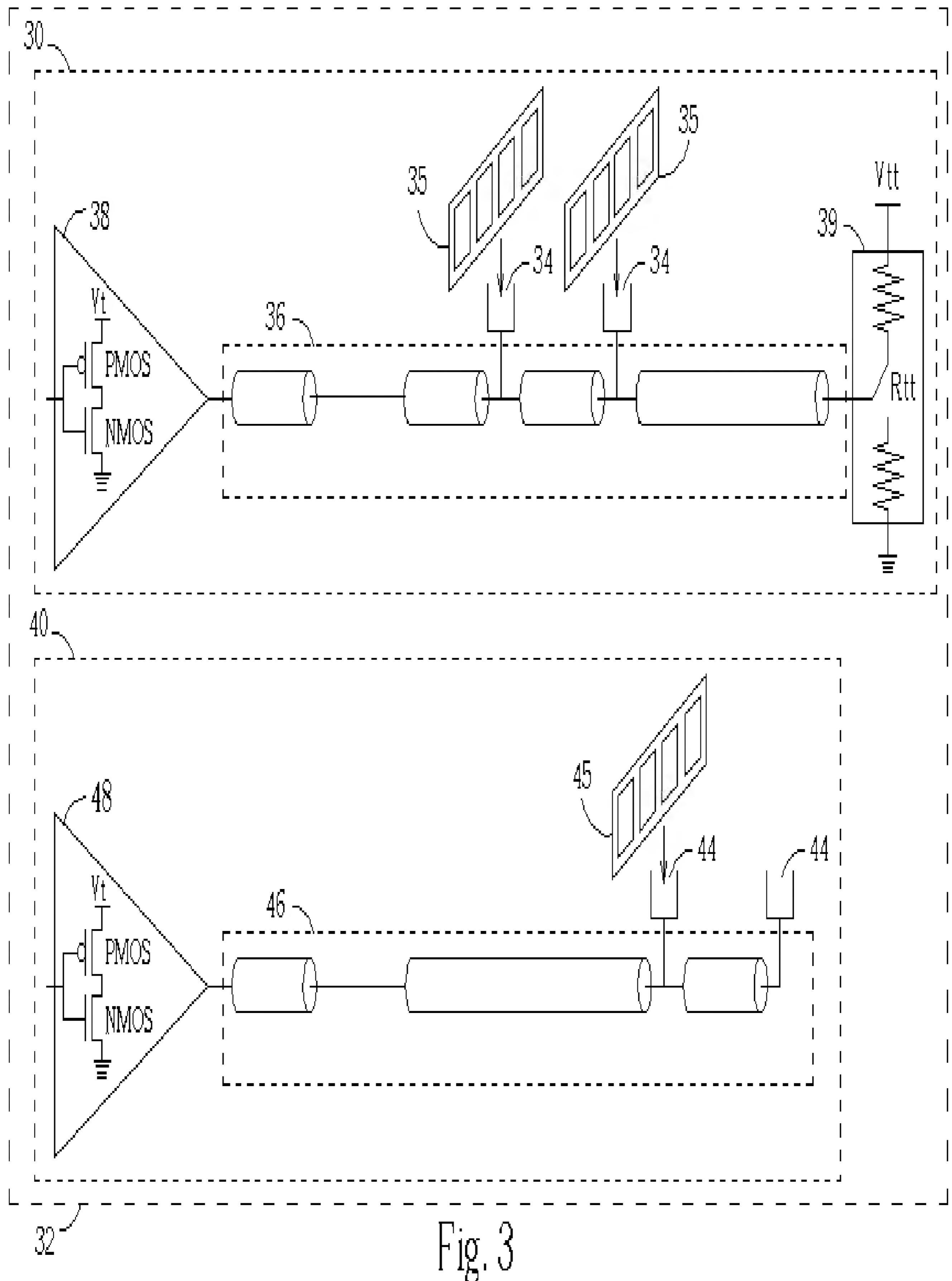


Fig. 2 Prior art



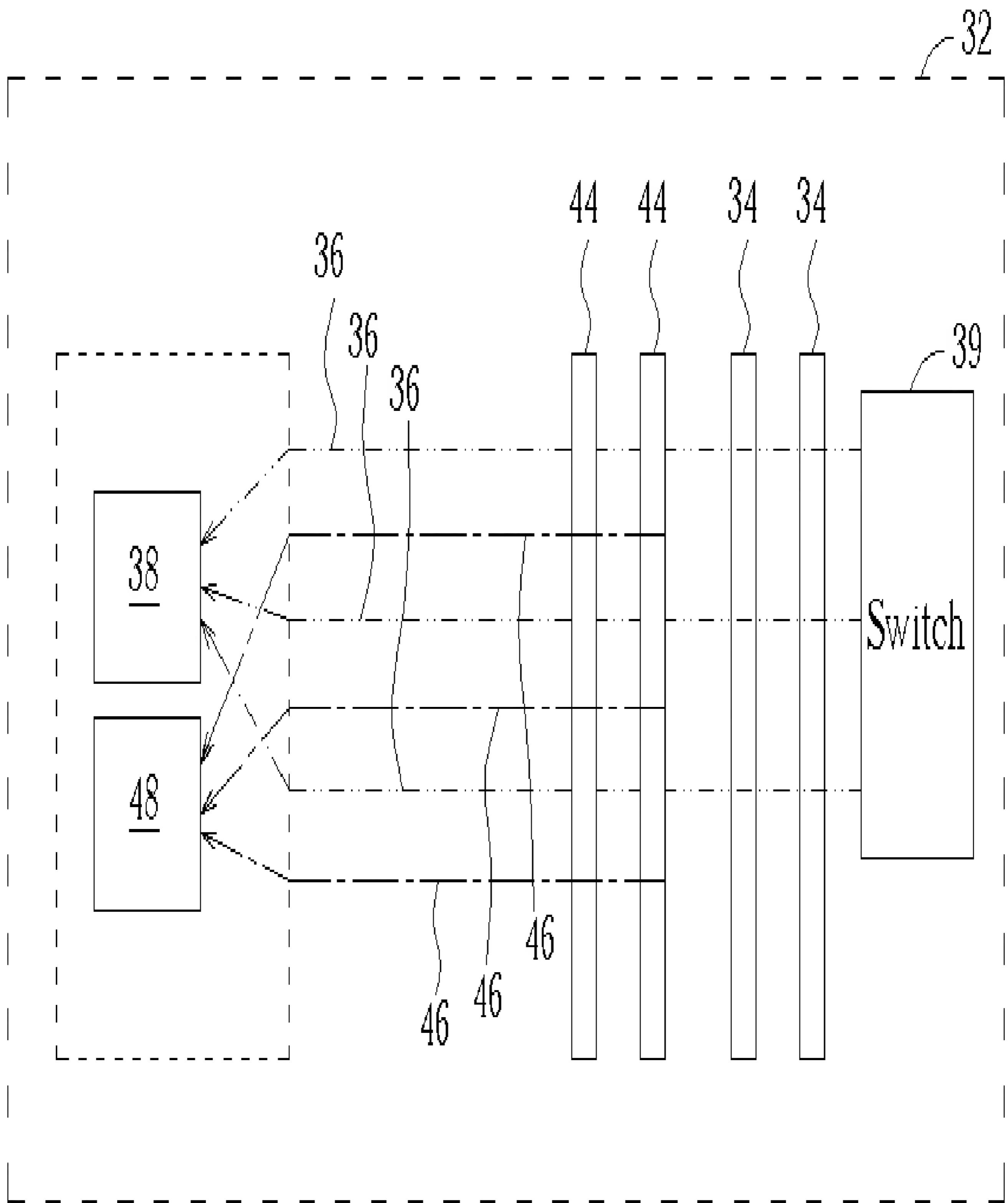


Fig. 4

Alternately lay out the (different) buses of the DDR I and the DDR II layout structure on the same electric board

100

Once utilize the buses of only one type to transmit signals and data

101

When the DDR I layout structure operates, electrically connect two ends of the buses of DDR II layout structure to the ground layer

102

When the DDR II layout structure operates, electrically connect two ends of the buses of DDR I layout structure to the ground layer

103

Fig. 5

